·2/4/8 channel synchronous sampling

·200/250Msps per channel，14bit

·Input bandwidth DC-100/120MHz

·Input range: can be set as ±1V、±2V、±5V、±10V

·Input impedance: 50Ω/1MΩ

·PCIe x8 Gen2 communication interface

·support FPGA secondary development

·Software development package supports C/C++，LabVIEW，Matlab etc

Series	Bus	Resolution	Channel	Sampling Rate	Bandwidth	Storage depth	System support
LD815x-xx14	PCIe x8 Gen2	14bit	2/4/8 channel	200/250Msps	DC-100/120 MHz	2GB	Windows Linux

Brief introduction

·LD815x-xx14 is Mysoow's "Agile" series PCIe acquisition card, supporting 2/4/8 channel, 14bit , achieving 200/250Msps synchronous sampling , On board DDR3 memory granules, with 2GB storage volume，larger volume customization is also supported.

·PCIe x8 Gen2 bus interface, supporting the data speed up to 20Gbps.

·Support multiple triggering modes including hardware , triggering, software triggering and manual triggering etc. Continuous sampling mode is also supported.

·Support the secondary development for users, providing DLL dynamic link pool.

·Equipped with host computer Demo software, supporting the configuration of board and real-time display and storage of data.

Typical applications

·Optical Coherence Tomography（OCT）

·Non-destructive detection

·Ware form recorder

·Multi-channel transient recorder

Detailed parameters

Terminal						
Simulated input	2/4/8 channel，standard SMB interface，SSMA optional					
Triggered input	1 way SMB					
Triggered output	1 way SMB					
Synchronous clock input	1 way SMB					
Synchronous clock output	1 way SMB					
Communication bus	PCIe x8 Gen2					
Acquisition system						
Resolution	14bit					
Bandwidth	DC-100/120MHz					
Input channel	2/4/8 channel synchronous sampling					
Sampling rate	up to 200/250Msps					
Sampling mode	Continuous sampling, finite point sampling					
Full range input scope	±1V、±2V、±5V、±10V					
Input coupling	AC/DC coupling					
Input impedance	50Ω/1MΩ					
Extreme input	±40Vmax					
Storage						
Storage volume	2GB					
Recording capability	Can be set up by software，the overall volume does not exceed memory space					
Trigger depth	Can be set up by software, the overall volume does not exceed memory space					
Index parameter						
Input impedance	Gear	SNR	SINAD	SFDR	ENOB	Test conditions
50Ω	±2V	68	68	70	11	Input signal amplitude-1dBFS，frequency 10MHz，sampling rate 200/250Msps
1MΩ	±2V	65	65	68	10.4	
Trigger Input System						
Trigger source	Software trigger, threshold(channel) trigger, external(simulation and digital) trigger					
Channel number	1 channel, supporting simulation and digital TTL, LVTTL，Input impedance 1MΩ					
Trigger input level	Simulation input ±0.2V~±5V, standard digital TTL, LVTTL level，square wave/pulse wave/trapezoidal wave					
Trigger frequency	≤2MHz					
Trigger mode	post-trigger, pre-trigger, delay-trigger, rising edge trigger, falling edge trigger, double-sided edge trigger					
Trigger threshold adjustment	±0.2V~±5V precisely adjustable					

Trigger input width	≥50ns
Trigger delay	0~2^31 sampling cycle
Trigger output	
Trigger output channel	1 channel，share terminal with clock output
Trigger output level	LVTTL，output current 10mA
Trigger output width	≥50ns，adjustable pulse width
Clock system	
Clock source	Internal/external/external direct sampling clock source
Feature of internal clock	10MHz，±2ppm
External input clock amplitude	0.4Vpp ~ 3.3Vpp sine wave or square wave
External input impedance	50Ω
External input coupling mode	AC coupling
External input frequency range	10MHz，10MHz~maximum sampling rate@external direct sampling mode
Clock output	10MHz@LVTTL，same source as sampling clock
Power requirements	
Power supply mode	PCIe insertion socket power supply，5557-2×3P connector power supply
Power requirement	12V/3A
Size and weight	
Size	Standard full height & full length PCIe board，occupying 1 bit PCIe insertion socket，length×width×height：180×20×111mm
Weight	~0.4kg
Environment parameters	
Working temperature	0℃ ~+50℃
Relative working humidity	10% ~ 90%RH, no condensation
Storage temperature	-40℃ ~+85℃
Relative storage humidity	5% ~ 95%RH, no condensation

System requirements

1> PCIe x8 Gen2 interface;

2> larger than 4GB memory，1GB hard-disk space;

3> Display screen resolution larger than 1280x1024;

4> Operation system supports Windows, Linux and domestically produced operation system;

Software Development Package

Provide software development package to help users to quickly accomplish application development and integration. MSDK software development package is applicable for Windows, Linux and other operation system, supporting the secondary integration and development for C/C++, Matlab, Labview, Python, C#, QT and other software, including host computer software, interface pool, DEMO routine and development description documents etc.

Ordering information

Ordering Information	
Model	Description
LD8152-200M14	2 channel 14bit，200Msps，PCIe x8 Gen2，bandwidth DC-100MHz
LD8152-250M14	2 channel 14bit，250Msps，PCIe x8 Gen2，bandwidth DC-120MHz
LD8154-200M14	4 channel 14bit，200Msps，PCIe x8 Gen2，bandwidth DC-100MHz
LD8154-250M14	4 channel 14bit，250Msps，PCIe x8 Gen2，bandwidth DC-120MHz
LD8158-200M14	8 channel 14bit，200Msps，PCIe x8 Gen2，bandwidth DC-100MHz
LD8148-250M14	8 channel 14bit，250Msps，PCIe x8 Gen2，bandwidth DC-120MHz

Note：

The product is not equipped with coaxial cables by default, and our company can provide customized wire services

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